

FIG. 1

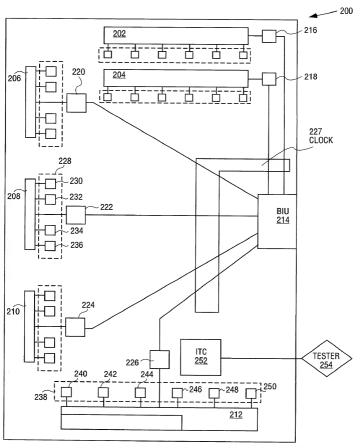
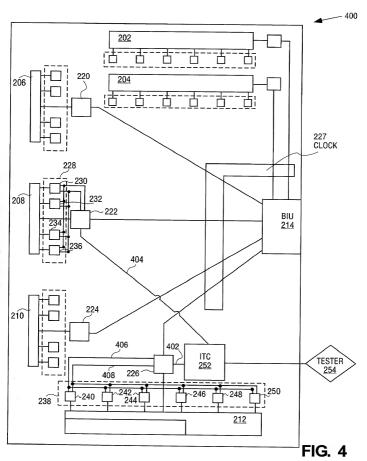


FIG. 2

(PRIOR ART)



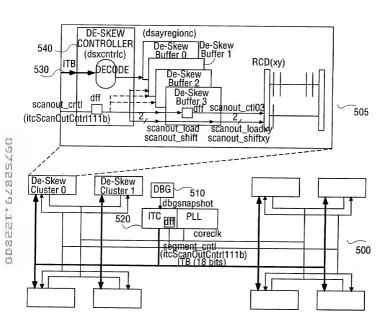


FIG. 5

control line/clock	Logic equations / details	Generated	Ω
scanout_load	SCANOUTSIG + SCANOUTLOAD + SCANOUTLOADDBG + De-skew	De-skew	all LBFs
(non_critical)	(ISCAN_BUS + ISCAN_TAP)	controller	
scanout_shift	SCANOUTSIG + SCANOUTSHIFT	De-skew	all BFs
(non_critical)		controller	
ck driving	ck (clock driving   scanout_ctl & qclk	LBF	LBF
scanout ff)			
scanout_ctl	1. (SCANOUTLOADDBG & Run-test/Idle	ITC. staged all I REc	all I REc
	+ (10	in De-skew	ב ב
	3. (SCANOUTSHIFT & (TCK & gclk)*) +	cluster	
	4. SCANOUTRST +		
	5. (SCANOUTSIG + Run-test/Idle & dbgsnapshot		
	6. (ISCAN_BUS + ISCAN_TAP)		

## FIG. 6

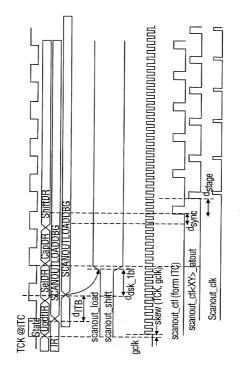


FIG. 7

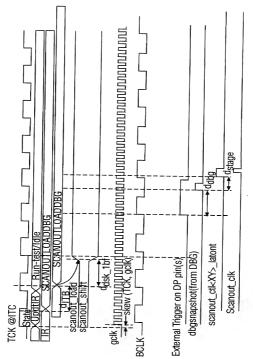


FIG. 8